L Number	Hits	Search Text	DB	Time stamp
1	5	current-differenc$3 NEAR circuit	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/01/17 18:36
2	7	trans-impedance NEAR stage	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/01/17 17:43
3	3578	(differential common) NEAR2 feedback	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/01/17 18:37
4	702	(differential common) NEAR2 (feedback ADJ circuit)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/01/17 18:38
6	0	((differential NEAR2 (feedback ADJ circuit)) WITH (common NEAR2 (feedback ADJ circuit))) and current-steering	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/01/17 18:49
5	37	(differential NEAR2 (feedback ADJ circuit)) WITH (common NEAR2 (feedback ADJ circuit))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/01/17 18:42
7	25	current-steering ADJ circuit	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/01/17 18:53
8	4	(current-steering ADJ circuit) and (reference ADJ circuit)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/01/17 18:53
-	1	("4021747").PN.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/01/17 12:52
-	17387	circuit same (low NEAR voltage) same (high NEAR voltage)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/01/17 12:57
-	252	(circuit same (low NEAR voltage) same (high NEAR voltage)) and (field NEAR transistor)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/01/17 13:01
-	238	circuit same (low NEAR voltage NEAR (input terminal)) same (high NEAR voltage NEAR terminal)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/01/17 14:06
-	2	(circuit same (low NEAR voltage NEAR (input terminal)) same (high NEAR voltage NEAR terminal)) and (field NEAR transistor)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/01/17 14:23
-	84	((circuit same (low NEAR voltage) same (high NEAR voltage)) and (field NEAR transistor)) and (source NEAR2 drain NEAR2 (gate control))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/01/17 14:03
-	21	(circuit same (low NEAR voltage NEAR (input terminal)) same (high NEAR voltage NEAR terminal)) and (source NEAR2 drain NEAR2 (gate control))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/01/17 14:22
-	2	circuit WITH (low NEAR voltage NEAR input) WITH (high NEAR voltage NEAR terminal)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/01/17 14:09

-	0	transistor WITH (low NEAR voltage NEAR input) WITH (high NEAR voltage NEAR terminal)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/01/17 14:10
-	2	transistor SAME (low NEAR voltage NEAR input) SAME (high NEAR voltage NEAR terminal)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/01/17 14:14
-	6	circuit SAME (low NEAR voltage NEAR input) SAME (high NEAR voltage NEAR terminal)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/01/17 14:16
-	2	circuit SAME transistor SAME (low NEAR voltage NEAR input) SAME (high NEAR voltage NEAR terminal)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/01/17 14:19
-	5905	circuit SAME transistor SAME (low NEAR voltage) SAME (high NEAR voltage)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/01/17 14:20
-	1564	circuit WITH transistor WITH (low NEAR voltage) WITH (high NEAR voltage)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/01/17 14:21
-	297	(circuit WITH transistor WITH (low NEAR voltage) WITH (high NEAR voltage)) and (source NEAR2 drain NEAR2 (gate control))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/01/17 14:22
-	123	((circuit WITH transistor WITH (low NEAR voltage) WITH (high NEAR voltage)) and (source NEAR2 drain NEAR2 (gate control))) and (field NEAR effect NEAR transistor)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/01/17 14:23
-	115	(((circuit WITH transistor WITH (low NEAR voltage) WITH (high NEAR voltage)) and (source NEAR2 drain NEAR2 (gate control))) and (field NEAR effect NEAR transistor)) and (input terminal)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/01/17 17:25